
TITLE **Phrasing a valid scope for an additional test loop**

PROJECT VDSL2

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STATUS for discussion

Rationale behind this contribution

The request for an additional VDSL2 test loop has been proposed several times within TM6, and raised again during this meeting in TD19 (072t19). Unfortunately, the acceptance of this proposal was *unnecessary* troubled by motivating it via controversial “arguments” (representing a 0.4mm cable, enable comparison with ADSL with VDSL2, is representative for Europe, etc). The huge differences in insertion loss between the 0.4mm loops shown in TD19, illustrate why this artificial loop cannot claim it represent a 0.4 loop (there is nothing 0.4 mm on the specification)
This kind of arguments has prevented progress, while a simple change in philosophy may open the way for a compromise within TM6.

The real motivation behind this loop is purely concentrated around achieving “*measurement convenience*” by enabling a common specification of ADSL loop simulators that are extended in frequency up to VDSL2 frequencies. There is nothing wrong with that argument, as long as that motivation is made *explicit* in ETSI document, and as long ETSI is not invited to publish “arguments” that are controversial.

Independent from the question what the specification of this additional loop should be, and independent from the question if it should be incorporated in the VDSL2 document of ETSI, such an ETSI document should make the specification informative, clarify explicitly the true motivation behind this additional loop, and be clear about its relation with the current (normative) VDSL2 test loop. Therefore we propose to use the following literal text, if ETSI accepts inclusion of that additional loop.

Start of literal text proposal

Informative annex X. Additional testloop <name neutral in gauge>

This additional loop is a 100% artificial loop, not related to any particular cable being deployed in Europe. It has been designed to represent a legacy ADSL testloop below 1 MHz (type [*]), and extended up to 30 MHz by using an educated guess on how a loop could behave. As such, it does not stress VDSL2 modems in a way that is fundamentally different from the set of normative VDSL2 test loops. However, this loop has been added purely for reasons of measurement convenience, to enable cable simulators dedicated to ADSL testing to offer meaningful characteristics up to VDSL2 frequencies.

*Continue with the specification of the additional loop,
without claiming that it represent a 0.4mm loop (“PE04”), since that cannot be proven*

End of literal text proposal